

## Specification for Mono LCD Display module

### 128 x 64 STN Monochrome Display module

Manufacturer	Yes Optoelectronics Co., Ltd
Part n°	YMS12864-04AHAYDCL
Ordering n°	YMS12864-04AHAYDCL
Customer Part n°	n/a
Revision n°	1.2
Issue Date	2013/10/26

### Customer's Approval

Company name	
Printed name	
Job title	
Signature	
Approval Stage:	<p>This product is approved for the following production stage: -</p> <p><input type="checkbox"/> Sample / Prototype</p> <p><input type="checkbox"/> Pre-Production</p> <p><input type="checkbox"/> Mass Production</p>
Approval Date	

Supplied by Anders Electronics plc  
 Manufactured by Yes Optoelectronics Co., Ltd



ANSHAN YES OPTOELECTRONICS DISPLAY CO.,LTD

**SPECIFICATIONS FOR  
LIQUID CRYSTAL DISPLAY MODULE**

MODEL NO.: YMS12864-04AHAYDCL      DATE: OCT.26.2013

Approved	Checked	Department

CUSTOMER:      ORTEK

MODEL NO.:

DATE:

Approved	Checked	Department

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<http://www.asiansources.com/sante.com>

DATE    OCT.26.2013

TECHNICAL SPECIFICATION

LCM

YES

YMS12864-04AHAYDCL

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## REVISION HISTORY

Rev.	Date	Item	Page	Content
1.0	OCT.10.2013	New Creation	ALL	
1.1	OCT.16.2013	Add the label on the bottom side	19-20	
1.2	OCT.26.2013	Modify the IC label	19-20	

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## I. General Specifications

### 1.Features

- A. Drive Method:1/64 Duty, 1/9 Bias
- B. The Module Operating Voltage:5.0V;
- C. Display mode: STN mode, Transfletive, positive type display
- D. The LCD Operating Voltage :9.7V;
- E. Viewing Direction: 6:00
- F. Operating Temperature: -20°C~70°C
- G. Storage Temperature: -30°C~80°C

### 2.Mechanical Data:

- (1) Module Size ----- 75.0 w \* 52.7 h mm
- (2) Viewing Area ----- 60.5 w \* 33.0 h mm
- (3) Dot Size ----- 0.40 w \* 0.40 h mm
- (4) Number of Dots -----128 \* 64 Dots
- (5) Outline Dimensions----- See Attached Drawing

### 3.Absolute Maximum Ratings

Characteristic	Symbol	Value	Unit	Note
Operating voltage	V <sub>DD</sub>	-0.3 to +7.0	V	(1)
Supply voltage	V <sub>EE</sub>	V <sub>DD</sub> -19.0 to V <sub>DD</sub> +0.3		(4)
Driver supply voltage	V <sub>B</sub>	-0.3 to V <sub>DD</sub> +0.3		(1),(2)
	V <sub>LCD</sub>	V <sub>EE</sub> -0.3 to V <sub>DD</sub> +0.3		(3),(4)

#### NOTES:

1. Based on V<sub>SS</sub>=0V
2. Applies to input terminals and I/O terminals at high impedance. (Except V<sub>0L</sub>(R), V<sub>1L</sub>(R), V<sub>4L</sub>(R) and V<sub>5L</sub>(R))
3. Applies to V<sub>0L</sub>(R), V<sub>1L</sub>(R), V<sub>4L</sub>(R) and V<sub>5L</sub>(R).
4. Voltage level: V<sub>DD</sub> ≥ V<sub>0L</sub>=V<sub>0R</sub> ≥ V<sub>1L</sub>=V<sub>1R</sub> ≥ V<sub>4L</sub>=V<sub>4R</sub> ≥ V<sub>5L</sub>=V<sub>5R</sub> ≥ V<sub>EE</sub>.

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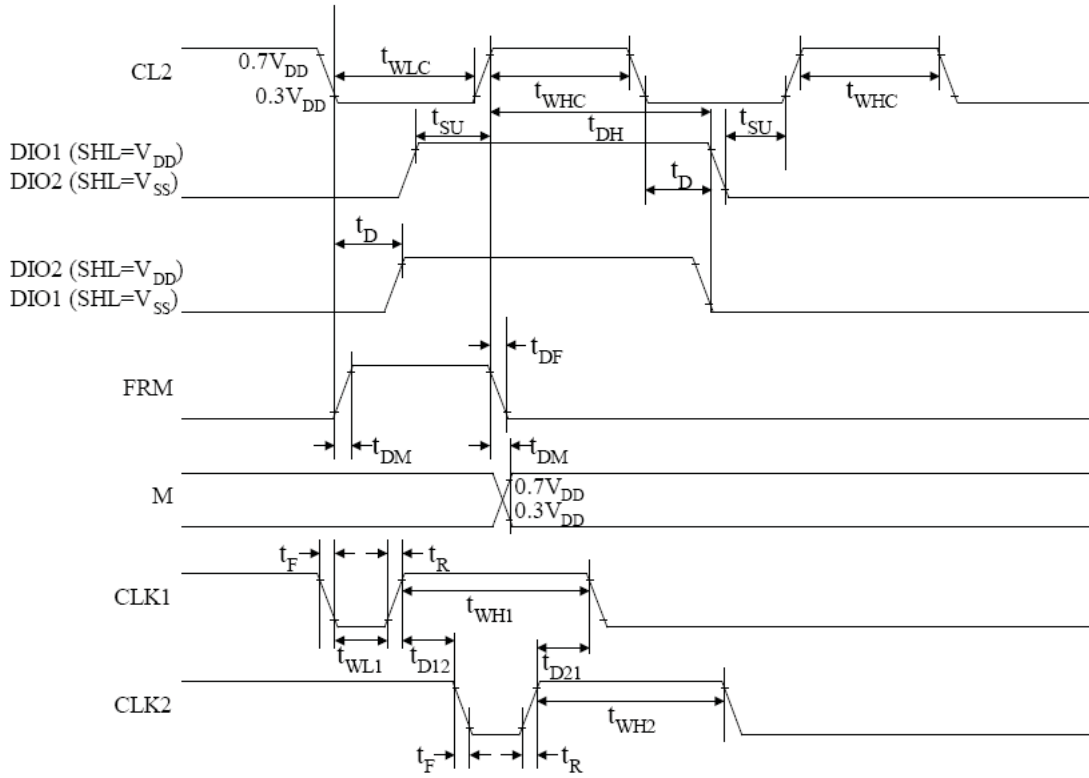
4.Pin Connections:

Pin No.	Symbol	Function
1	VSS	Ground(0v)
2	VDD	Logic Supply Voltage(+5.0V)
3	VEE	LCD Driver Voltage Input(+9.7V)
4	RS	Data Or Instruction
5	R/W	Read/Write Select
6	E	Enable Signal
7~14	DB0~DB7	Data Bus Line
15	CS1	Chip Selection(Segment Driver 1)
16	CS2	Chip Selection(Segment Driver 2)
17	RSTB	Reset Signal
18	Vout	Dc-Dc-Converter Output
19	LED +	LED Backlight anode
20	LED-	LED Backlight cathode

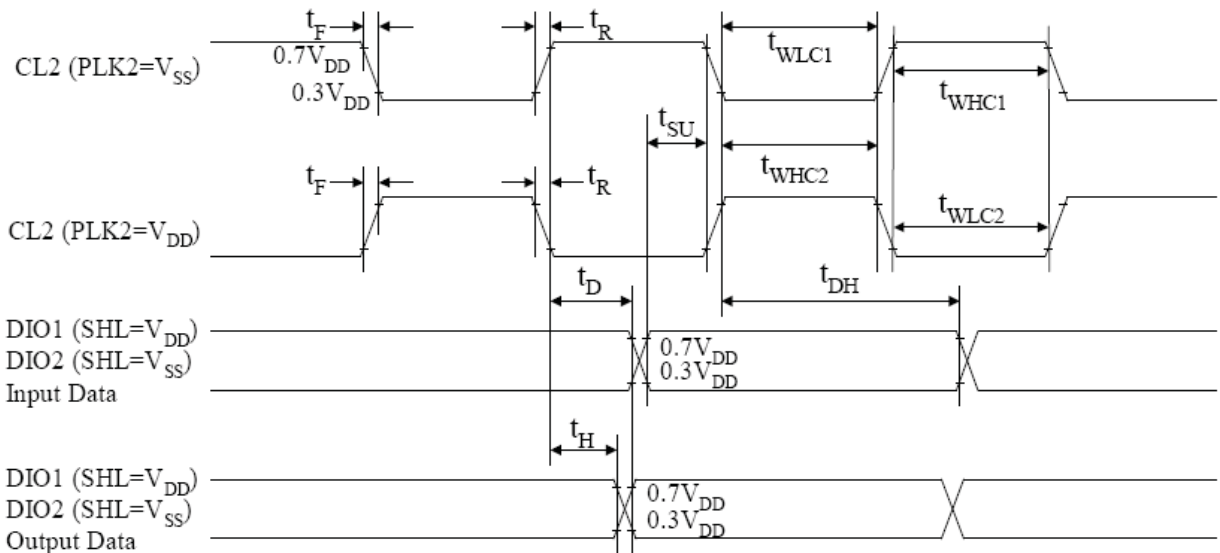
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5.AC Characteristics:(VDD=5V ± 10%)

**Master Mode (MS=V<sub>DD</sub>, PCLK2=V<sub>DD</sub>)**



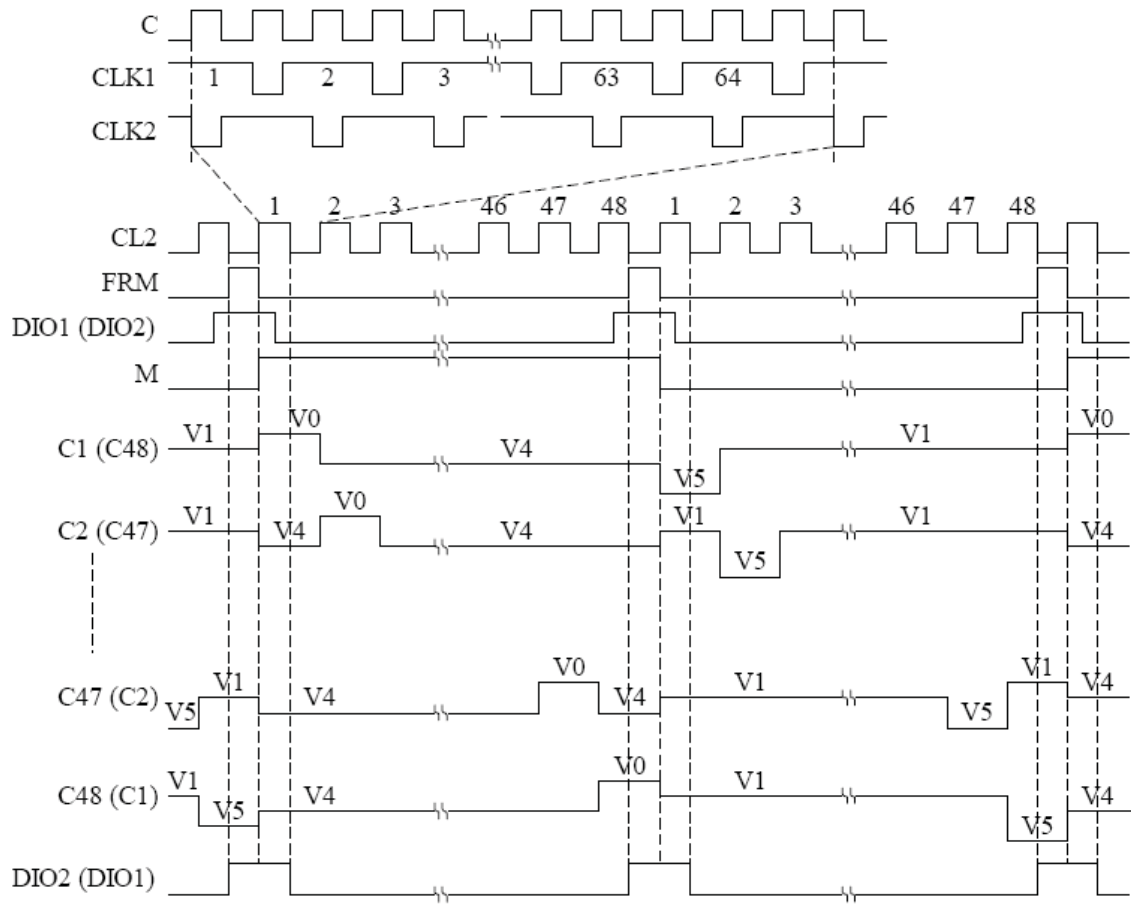
**Slave Mode (MS=V<sub>SS</sub>)**



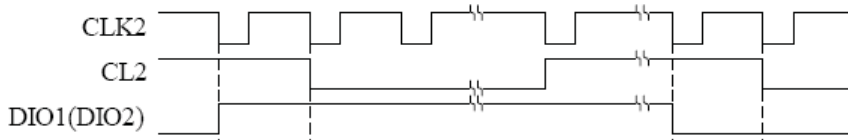
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### 1/48 DUTY TIMING (MASTER MODE)

Condition: DS1=L, DS2=L, SHL=H (L), PCLK2=H



Relation of CL2 and DIO1(DIO2)

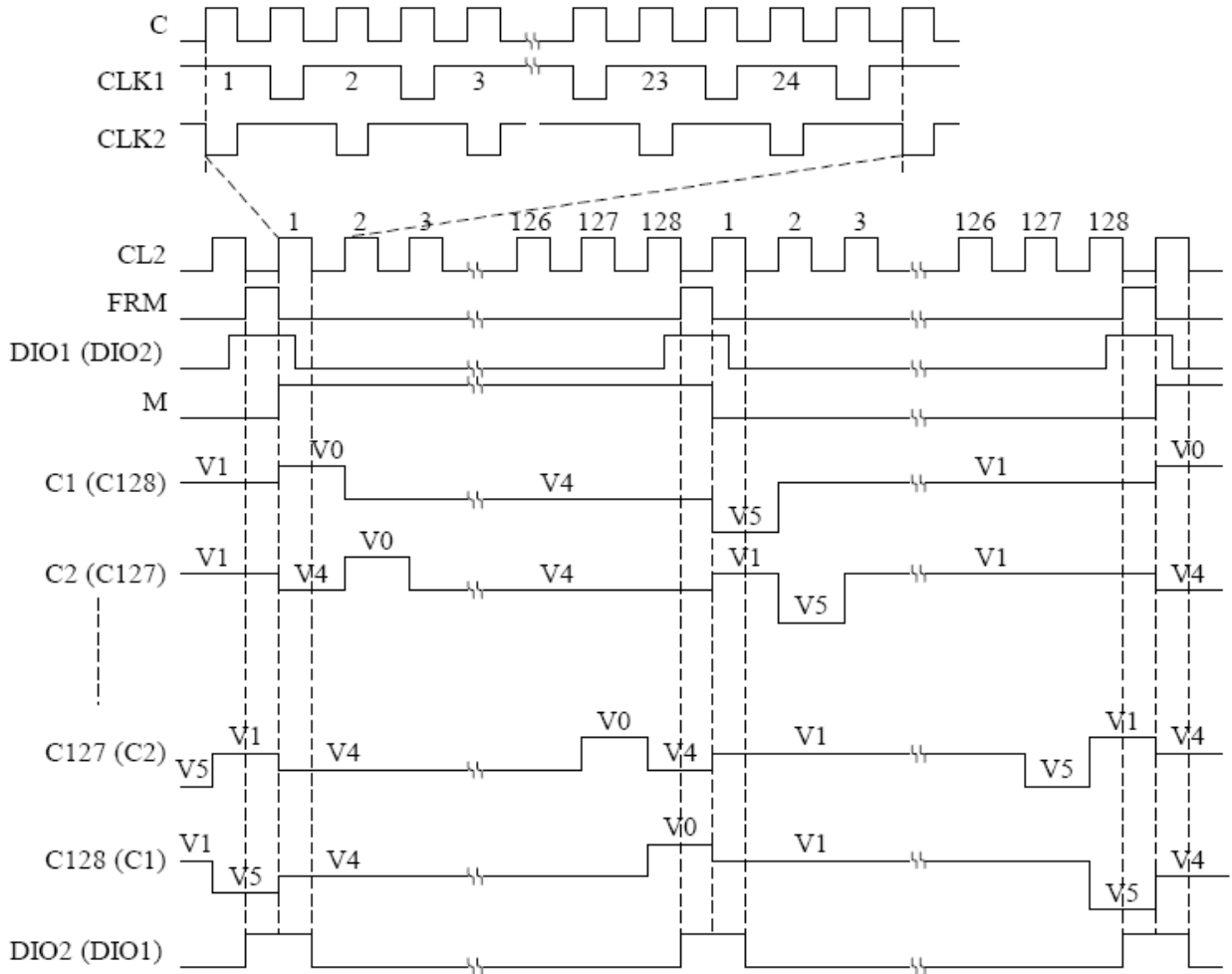


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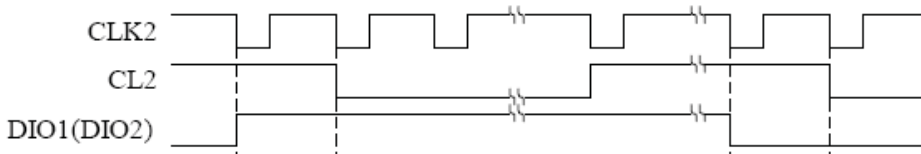


### 1/128 DUTY TIMING (MASTER MODE)

Condition: DS1=H, DS2=H, SHL=H(L), PCLK2=H



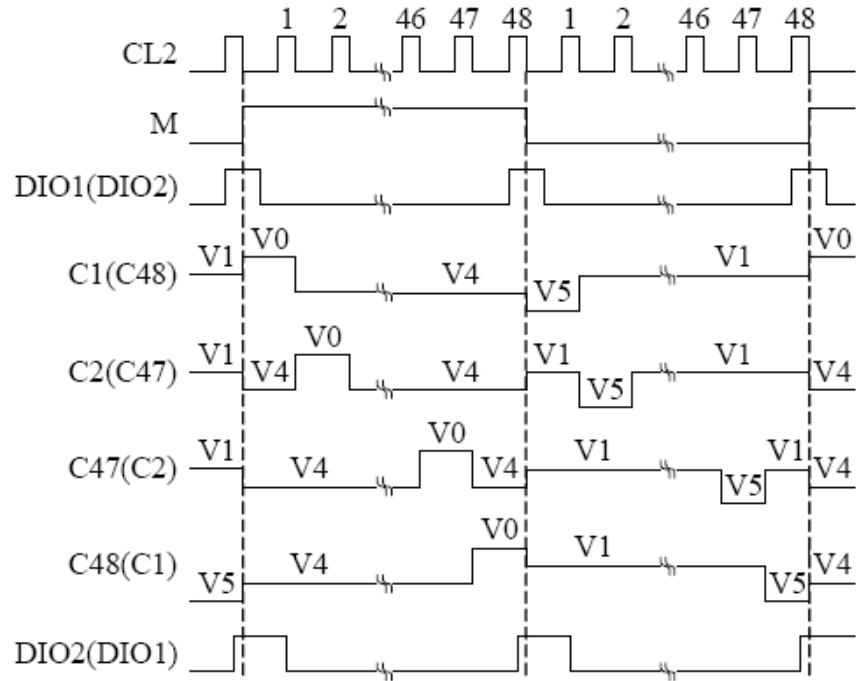
Relation of CL2 and DIO1(DIO2)



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### 1/48 DUTY TIMING (SLAVE MODE)

Condition: SHL=H (L), PCLK2=L



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## II.The Characteristics and The Reliability Test

### 1.Electro-Optic Characteristics:

Condition: TEMP=(23±3)°C Hum=(70±5)%RH

V<sub>dd</sub>: 5.0V

#### Evaluate value

NO	Item	Symbol	Min	Typ.	Max	Unit	Condition
1	Supply Voltage(Logic)	V <sub>dd</sub> -V <sub>ss</sub>		5.0		V	
2	LCD Operating Voltage	V <sub>dd</sub> -V <sub>0</sub>		10.1		V	-20°C
			9.5	9.7	9.9	V	25°C
				9.3		V	70°C
3	Response Time	T <sub>on</sub>		97		ms	
		T <sub>off</sub>		131		ms	
4	Contrast	CR	2	3			
5	Viewing Angel	12H	θ 1	40		Deg	(CR≥3.0)
		6H	θ 2	58			
		3H	θ 3	55			
		9H	θ 4	55			

### 2. Characteristics of backlight (LED unit)

Color: Yellow Green

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
Forward Voltage	V <sub>f</sub>	—	4.2	4.6	V	I <sub>f</sub> =100mA
Forward Current	I <sub>f</sub>	—	100	200	mA	
Power Dissipation	P <sub>d</sub>	—	—	0.92	W	I <sub>f</sub> =100mA
Reverse Voltage	V <sub>r</sub>	—	—	10.0	V	
Reverse Current	I <sub>r</sub>	—	—	0.30	mA	
Luminous Intensity	I <sub>v</sub>	20	—	—	cd/m <sup>2</sup>	I <sub>f</sub> =100mA
Luminous Uniformity	ΔL <sub>v</sub>	70	—	—	%	I <sub>f</sub> =100mA
Emission Wavelength	λ <sub>p</sub>	568	—	572	nm	I <sub>f</sub> =10mA Ta=25° C Each chip

#### WARNING:

A BACKLIGHT IS A KIND OF CURRENT DEVICE,IT MUST CONNECT WITH A RESISTOR FOR LIMITING CURRENT ,OR IT WILL BE DAMAGED

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### 3. Reliability Test

No	Items	Test Condition	Test Result
1	High TEMP Storage	TEMP: $80 \pm 2^{\circ}\text{C}$ Time: 96h Restore:24h	Passed
2	Low TEMP Storage	TEMP: $-30 \pm 3^{\circ}\text{C}$ Time: 96h Restore:24h	Passed
3	High TEMP Operating	TEMP: $70 \pm 2^{\circ}\text{C}$ Vop: 5V Timp: 24h Restore:24h	Passed
4	Low TEMP Operating	TEMP: $-20 \pm 2^{\circ}\text{C}$ Vop: 5V Timp: 24h Restore:24h	Passed
5	High TEMP High Hum Storage	TEMP: $40 \pm 2^{\circ}\text{C}$ Hum: 95%Rh Time: 96h Restore:24h	Passed
6	Thermal Shock	TEMP:( $^{\circ}\text{C}$ ) <p>70<math>^{\circ}\text{C}</math> 25<math>^{\circ}\text{C}</math> -20<math>^{\circ}\text{C}</math></p> <p>30 5 30 5 Min</p> <p>5 Cycles Restore:24h</p>	Passed

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TECHNICAL SPECIFICATION

LCM

YES

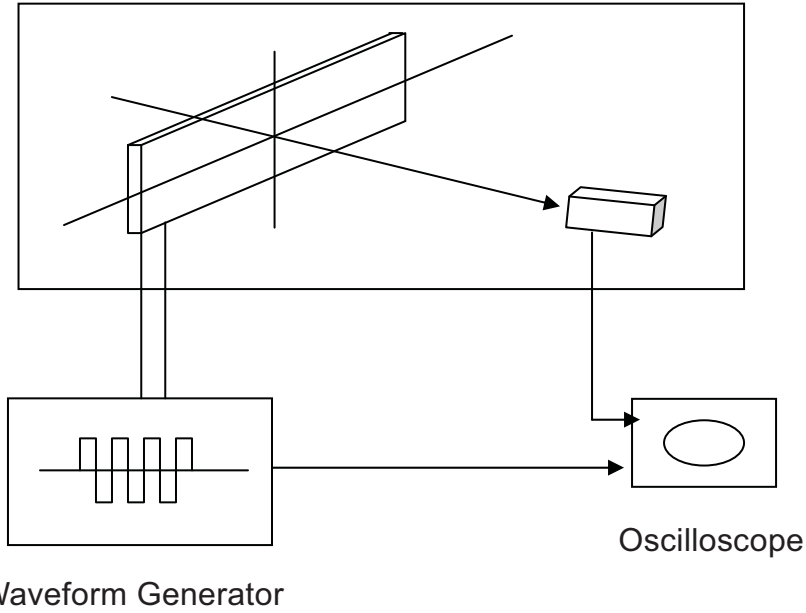
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### III.The LCD Measuring Method and Equipment

#### 1. Threshold Voltage and Response Time Measuring

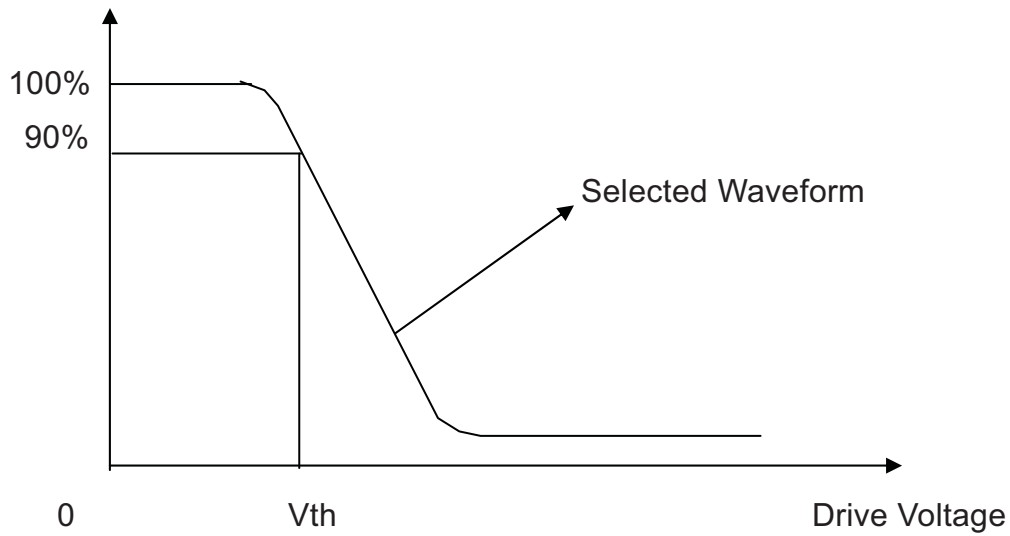
##### (1) Equipment



##### (2) Definition

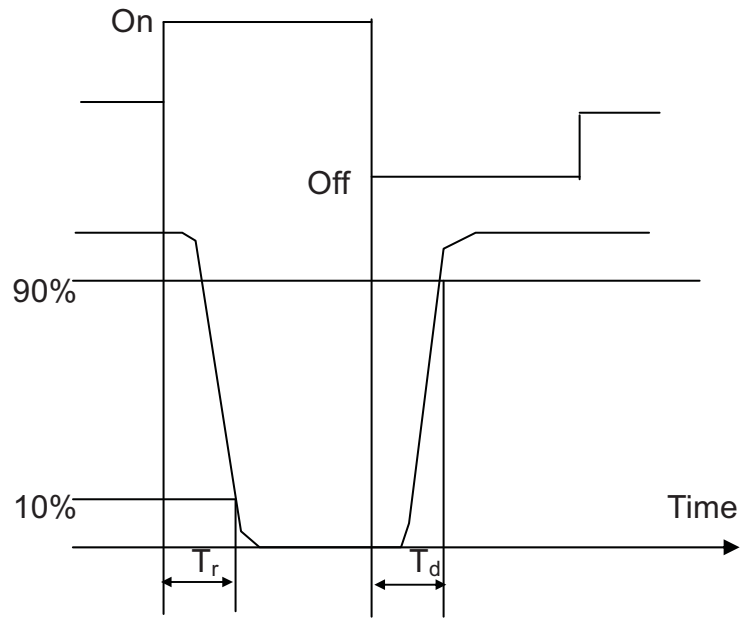
##### A. Threshold Voltage ( $V_{th}$ )

Brightness

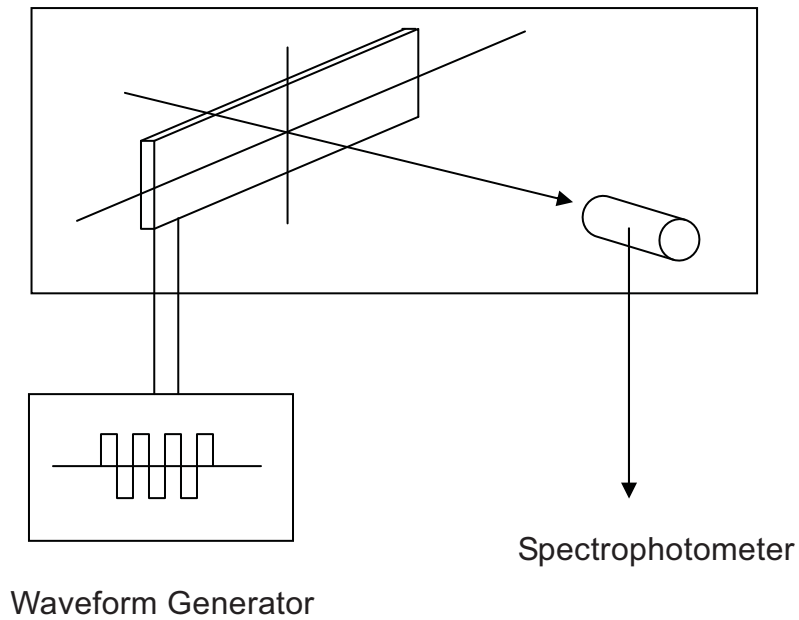


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**B. Response Time**



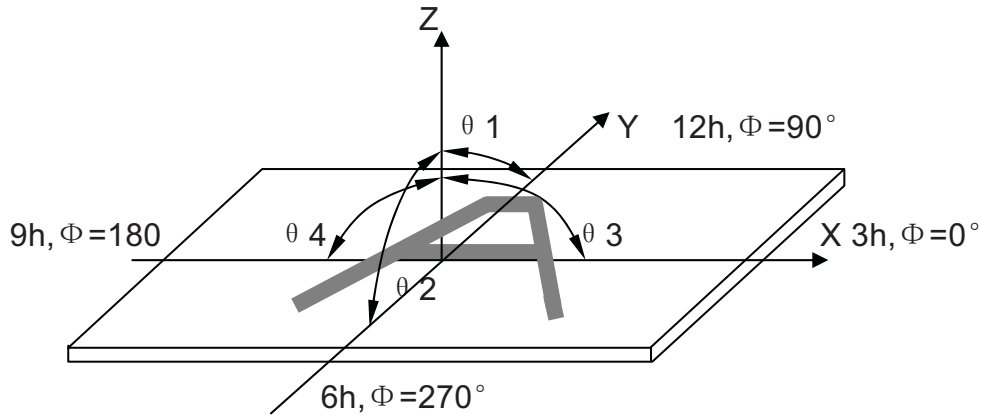
**2. Contrast Measuring**  
**(1) Equipment**



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(2)Definition:

A. Viewing Angle:



B. Contrast Ratio (Positive)

$$CR = \frac{\text{Brightness of non-selected wave-form}}{\text{Brightness of selected wave-form}}$$

3. Reliability Test:

Equipment : TENNY

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### IV. Standard Specifications for Product Quality

1. Manner of test::

- 1.1 The test must be under 40W fluorescent light, and the distance of view must be at 30cm.
- 1.2 The test direction is based on around -10°- 30° of Vertical line.

2. Quality specification

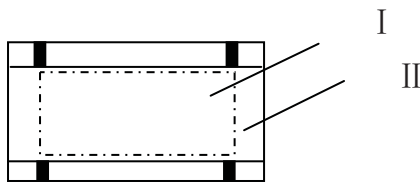
It shall be based on GB2828-87, Apply level II, Normal inspection by single sampling.

	IETM	CHECK LEVEL	AQL
MAJOR (MA)	1.LIQUID CRYSTAL LEAKAGE 2.WRONG POLARIZER 3.OUTSIDE DIMENSION 4.SEGMENT MISSING 5.SEGMENT SHORT	II	0.25
MINOR (MI)	1.BLACK SPOTS OR WHITE SPOTS. 2.FOREIGN SUBSTANCE, 3.WHITE SPOTS, 4.PINHOLE,SEGMENT 5.DEFORMATION SCRATCHS(GLASS & POLARIZER), 6.SEGMENT DEFECT, 7.AIR BUBBLES BETWEEN GLASS & POLARIZER, 8.COLOR VARIATION, GLASS CHIPS, 9.OTHER VISUAL DEFECTS.	II	1.0

3. Definition of area:

3.1 I area: viewing area

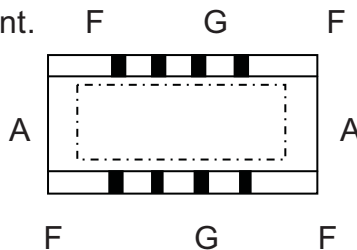
II area: outside viewing area



3.2 A area: The glass area outside sealant.

G area: Electrode pad area.

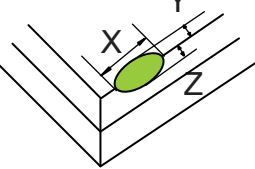
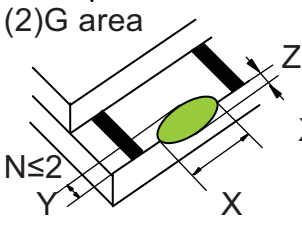
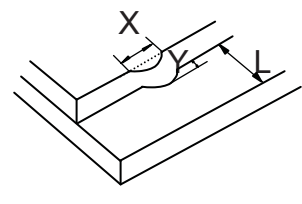
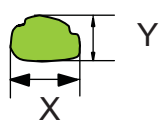
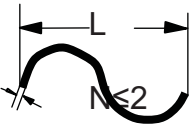
F area: Without electrode pad area.



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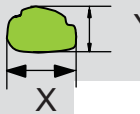
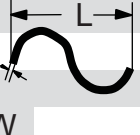
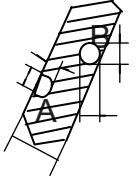
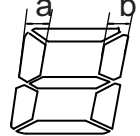
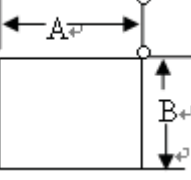


4. Standard of appearance test: (unit: mm)

№	Items	Criterion	Checking manner
1	<p>Substrate crack</p> <p>X: defect Length</p> <p>Y: defect Width</p> <p>Z: defect Depth</p> <p>T: glass Thickness</p> <p>N: defect QTY</p> <p>L: Connector Width</p>	<p>(1) A area</p>  <p><math>X \leq 3.0</math> Y: Don't allowed hurt sealing <math>Z \geq T/2</math> <math>N \leq 3</math></p> <p><math>X \leq 5.0</math> Y: Don't allowed hurt sealing <math>Z \leq T/2</math> <math>N \leq 3</math></p> <p><math>X \leq 1.0</math> <math>Y \leq 0.5</math> <math>Z \leq T/3</math> No check</p> <p>(2) G area</p>  <p><math>X \leq 3.0</math> <math>Y \leq 0.5</math> <math>Z \leq T/2</math></p> <p><math>N \leq 2</math></p> <p>(3) F area</p>  <p><math>X \leq \text{total length}</math></p> <p><math>Y \leq 1/4L</math> <math>N \leq 1</math></p> <p>Over the drawing tolerance is not allowed</p> <p><math>X \leq 2.0</math> <math>Y \leq 3</math> <math>Z \leq T</math> <math>N \leq 3</math></p> <p>Don't allowed hurt</p>	<p>checking with eyes</p>
2	<p>Black spot white spot <math>D = (X+Y)/2</math></p> <p>Line</p>	<p>(1)</p>  <p><math>0.2 &lt; D \leq 0.25</math> <math>N \leq 1</math></p> <p><math>0.1 &lt; D \leq 0.2</math> <math>N \leq 3</math></p> <p><math>D \leq 0.1</math> No check</p> <p>(2)</p>  <p><math>L \leq 2.0</math> <math>W \leq 0.03</math></p> <p><math>L \leq 1.0</math> <math>W \leq 0.05</math></p> <p><math>N \leq 1</math></p> <p>W</p>	<p>Checking on the table with light and polarizer and checking with eyes directly.</p>

No	Items	Criterion	Checking manner
3	Polarizer Bubble	$D \leq 0.15$ No check $0.15 < D \leq 0.4$ $N \leq 2$	Checking on the table with light and polarizer, and checking with eyes directly
4	Rainbow Color	Allow tiny rainbow Allow 5% color contrast or accord limitative sample	Checking on the table with light and polarizer, And checking with eyes directly
5	END Seal	1. Dimension accord design require 2. Inject depth (d): $1/5D \leq d \leq D$ (D: seal design depth)	Checking with eyes
6	Polarizer or pad appearance	No dirty	Checking with eyes

5 Standard of display test

No	Items	Criterion	Checking manner
1	Black spot white spot $D = (X+Y)/2$  Line	<p>(1)  <math>0.2 &lt; D \leq 0.25</math> <math>N \leq 1</math> <math>0.1 &lt; D \leq 0.2</math> <math>N \leq 3</math> <math>D \leq 0.1</math> No check</p> <p>(2)  <math>L \leq 2.0</math> <math>W \leq 0.03</math> <math>N \leq 2</math> <math>L \leq 1.0</math> <math>W \leq 0.05</math> <math>N \leq 1</math></p>	Checking at the display state
2	Pin hole $D = (A+B)/2$ W: segment width	 <p><math>W \leq 0.4</math> <math>D \leq 0.20</math> And <math>D \leq 1/2W</math> <math>N \leq 1</math> <math>W &gt; 0.4</math> <math>D \leq 0.25</math> And <math>D \leq 1/3W</math> <math>N \leq 2</math> <math>D \leq 0.05</math> No check</p>	Checking at the display state
3	Different width of segment	 <p><math> a-b  &lt; 0.25</math> or <math> a-b  \leq 1/4W</math> No check</p>	Checking at the display state
4	Different width	 <p>A: distortion <math>\leq 10\%</math> B: distortion <math>\leq 10\%</math> Superfluous Electrode lines display is not allowed</p>	

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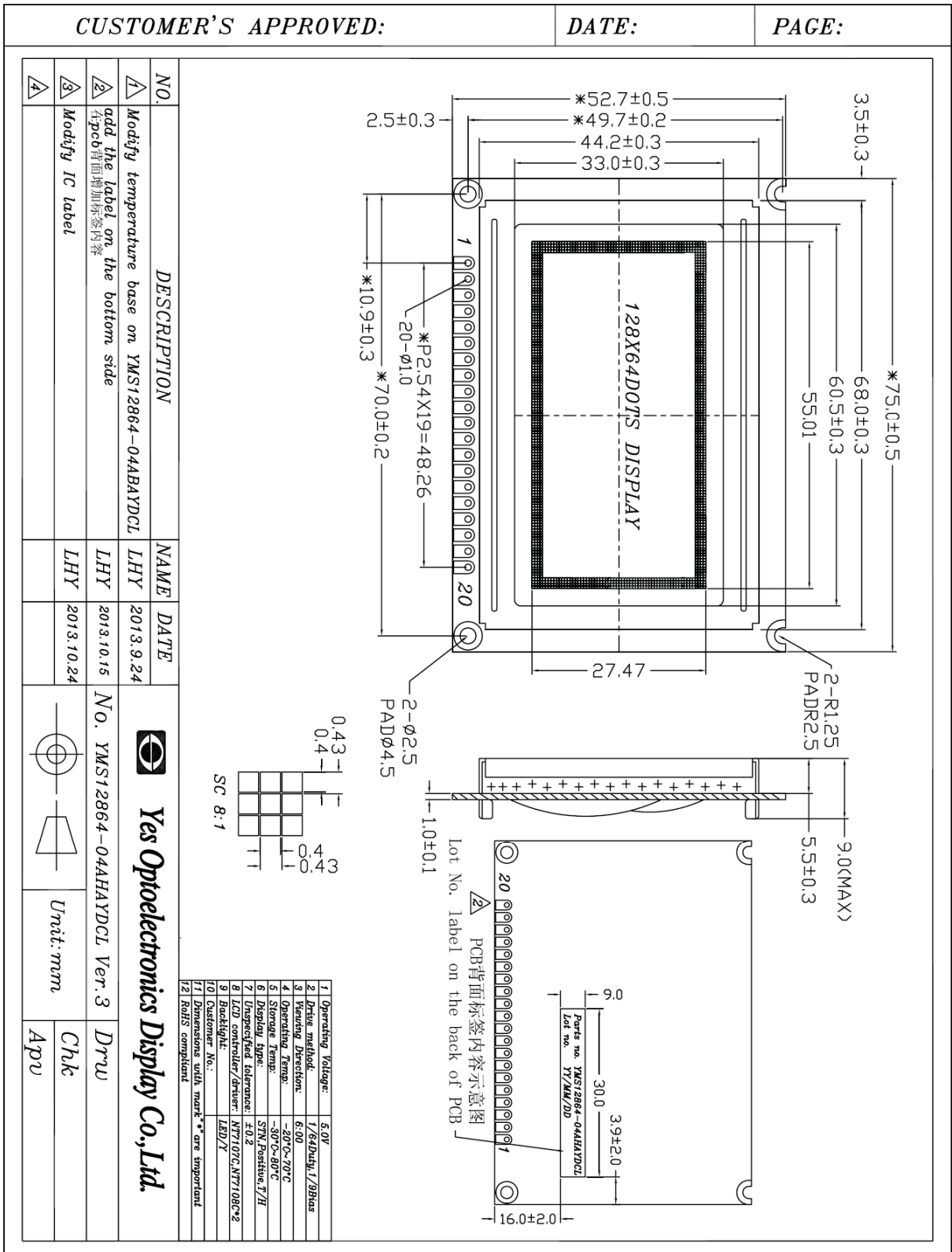
5	Pinhole	$\Phi = (A+B) / 2$	$0.15 < \Phi \leq 0.2 \quad N \leq 1$ $0.05 < \Phi \leq 0.15 \quad N \leq 3$ $\Phi \leq 0.05$ Any number Note: Distance between two spots $\geq 10\text{mm}$ , $\Phi < 1/3$ pixels
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**6. Inspection Item and Standards**

Item	The Standard Of Quality Inspection	Checking Method	Quantity Ratio
Frame	Smooth and even surface, no crack, no scratch, no rusty, and not be wrenched out of shape. the range between convex and concave is: $d \leq 0.35\text{mm}$ , and the frame must be connected with the ground pad.	Checking With Eyes And Using Vernier Caliper, Multimeter	100%
The Relative Position of LCD and Frame	The end seal of the LCD must be at the same side with the frame's opening.	Checking With Eyes	100%
The Relative Position of PCB/Panel /Frame	The frame installing direction must be correct. the twisted angle of the leg is from $45^\circ$ to $60^\circ$ , the leg is vertical to PCB panel and it must be in the middle position of the installing holes.	Checking With Eyes	100%
LED	1. The LED must be Yellow Green. 2. The LED must be uniform.	Checking With Eyes	100%
Function Test	1. The major defects must be reject. 2. Background changes evenly and no disorderly displaying phenomenon. 3. Display no shortage.	Check It When Displaying	100%

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V.Attached Drawing



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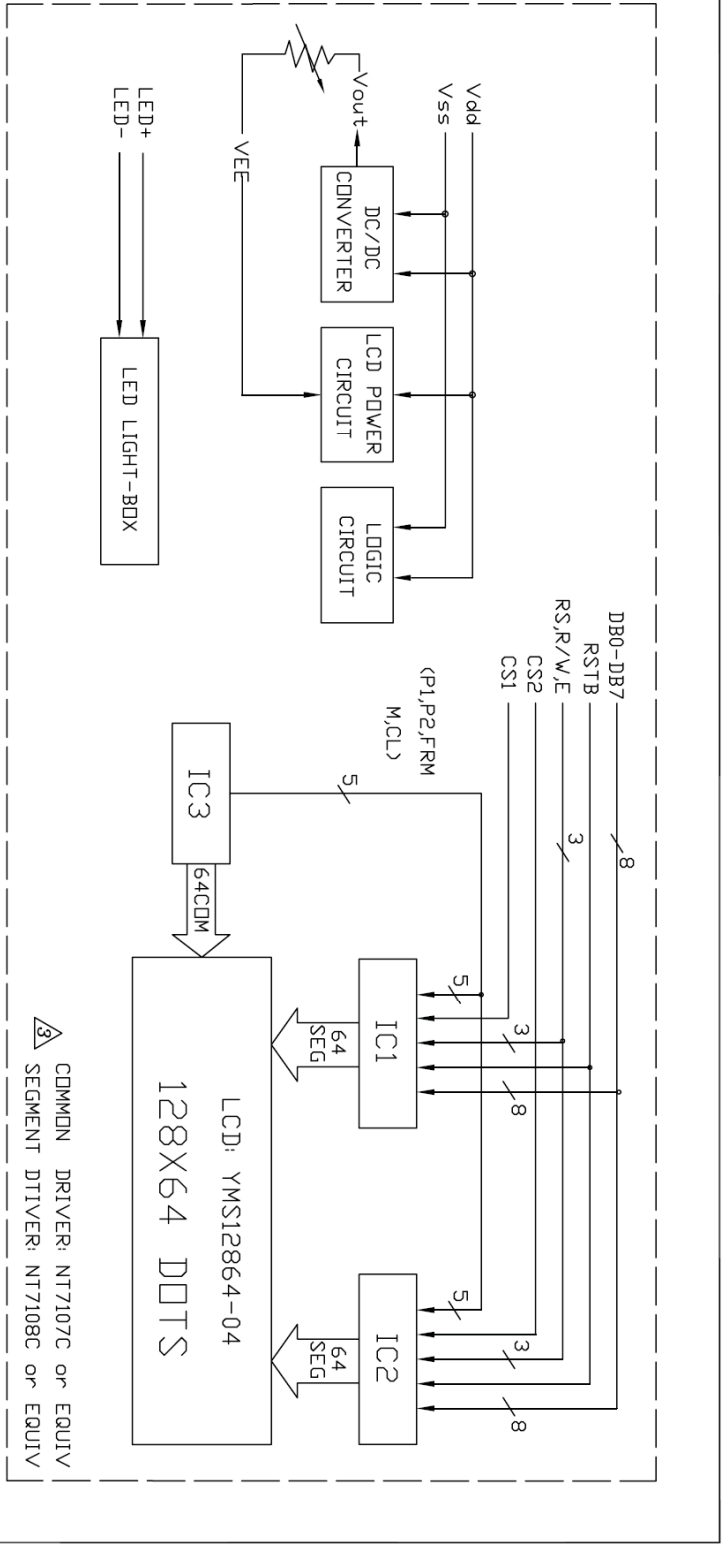
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CUSTOMER'S APPROVED:

DATE:

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PIN NO.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
SYMBOL	V <sub>SS</sub>	V <sub>DD</sub>	VEE	RS	R/W	E	DB0	DB1	DB2	DB3	DB4	DB5	DB6	DB7	CS1	CS2	RSTB	V <sub>out</sub>	LED+	LED-



**Yes Optoelectronics Display Co., Ltd.**

No. YMS12864-04AHAYDCL Ver. 3

	Unit: mm	Draw
		Aprv

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## VI. Packing

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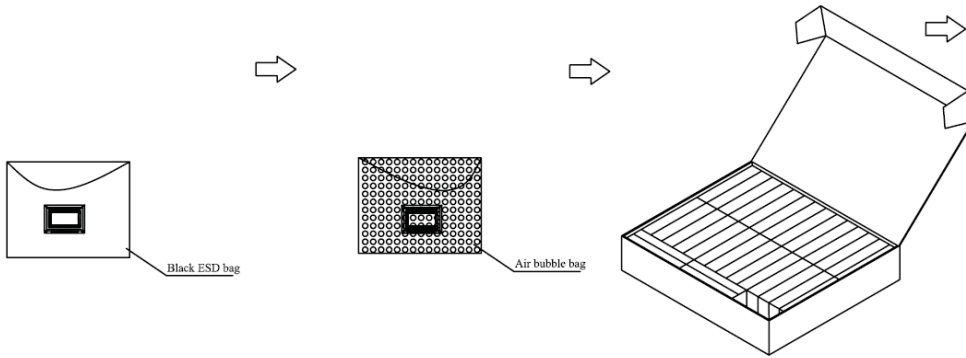
PRODUCT PART NO.: YMS12864-04AHAYDCL

**Packing Process:**

1) Putting Modules into each black ESD bag.

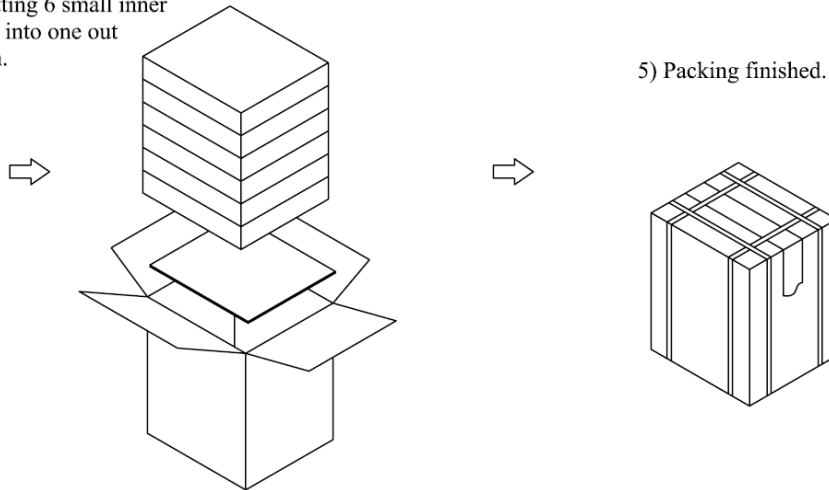
2) Putting Modules with black ESD bag into the air bubble bag.

3) Putting 90 pcs Modules into the inner box (TYPE:H67) and space filled filling piece.



4) Putting 6 small inner boxes into one out carton.

5) Packing finished.



Note: 90x6=540pcs/Outcarton

Dimension (Small carton): 385\*325\*72mm

Dimension (Out carton): 394\*344\*470mm

NO. YMS12864-04AHAYDCL

Drw:

Chk:

Apv:

**ANSHAN YES OPTOELECTRONICS DISPLAY CO., LTD**

DATE OCT.26.2013

TECHNICAL SPECIFICATION

LCM

YES

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**VII.Precautions For Use**

**1. Safety**

- (1) Do not swallow any liquid crystal, even if there is no proof that liquid crystal is poisonous.
- (2) If the LCD panel breaks, be careful not to get liquid crystal to touch your skin.
- (3) If skin is exposed to liquid crystal, wash the area thoroughly with alcohol or soap.

**2.Storage Conditions**

- (1) Store the panel or module in a dark place where the temperature is  $25\pm 5^{\circ}\text{C}$  and the humidity is  $50\pm 20\% \text{RH}$ .
- (2) Store in anti-static electricity container.
- (3) Store in clean environment, free from dust, active gas, and solvent.
- (4) Do not place the module near organics solvents or corrosive gases.
- (5) Do not crush, shake, or jolt the module.
- (6) Do not exposed to direct sun light of fluorescent lamps.

**3.Installing LCD Module**

Attend to the following items when installing the LCM.

- (1) Cover the surface with a transparent protective plate or touch panel to protect the polarizer and LC cell.
- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements.

**4.Precautions For Operation**

- (1) Viewing angle varies with the change of liquid crystal driving voltage ( $V_o$ ). Adjust  $V_o$  to show the best contrast.
- (2) Driving the LCD in the voltage above the limit will shorten its lifetime.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.

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(4) When turning the power on, input each signal after the positive/negative voltage becomes stable.

(5) Do not apply water or any liquid on product which composed of T/P.

5.Handling Precautions

(1) Avoid static electricity which can damage the CMOS LSI; please wear the wrist strap when handling.

(2) The polarizing plate of the display is very fragile. so, please handle it very carefully.

(3) Do not give external shock.

(4) Do not apply excessive force on the surface; it may cause display abnormal .

(5) Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.

(6) Do not use ketonics solvent & Aromatic solvent, use with a soft cloth soaked with a cleaning naphtha solvent.

(7) Do not operate it above the absolute maximum rating.

(8) Do not remove the panel or frame from the module.

(9) Do not apply water or any liquid on product which composed of T/P.

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